

Patent 7,624,156

PATENT

IN UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.:	7,624,156	Docket No:	P8193
Filing Date:	May 23, 2000	Serial No.:	09/576,038
Issue Date:	November 24, 2009	Inventors:	Mark Sean Hefty et al.
Assignee:	Intel Corporation		
Title:	METHOD AND SYSTEM FOR COMMUNICATION BETWEEN MEMORY REGIONS		

REQUEST FOR CERTIFICATE OF CORRECTION

Attn: Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

It is requested that a Certificate of Correction be issued correcting errors appearing in the above-identified United States Patent. Two copies of the text of the Certificate in the suggested form are enclosed.

Pursuant to 1.20(a), the examiner is authorized to charge the Certificate of Correction fee of \$100.00 to the Deposit Account No. **50-4238**.

It is believed that issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

Respectfully submitted,

Customer Number: 76973

/Christopher K. Gagne, Reg. No. 36,142/

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 7,624,156

APPLICATION NO.: 09/576,038

ISSUE DATE : November 24, 2009

INVENTOR(S) : Mark Sean Hefty et al.

It is certified that errors appear in the above-identified Patent and that said Letters Patent is hereby corrected as shown below:

In column 13, line 49, in Claim 7, insert --:-- (colon) after "to" (second occurrence).

In column 13, line 62, in Claim 7, delete "sizes" and insert - - size, - -, therefor.

In column 15, line 1, in Claim 9, after "the" insert - - data - -.

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